Computer Organization and Networks
(INB.06000UF, INB.07001UF)

Chapter 13: Information on the Exam

Winter 2022/2023

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The Written Exams

- All written exams take 90 minutes

- Questions are in English

- You can write your answers in English or German

- Paper and pen only (no calculator, mobile, …) – no red ink, no pencil

Important!

- **Please deregister from an exam immediately, if you decide to not take part in an exam!!!**

- It significantly facilitates planning large exams (number and type of lecture halls), if the number of participants is known

Currently Planned Exams

- 8th of February 2023

- 2nd of March 2023

- 24th of April 2023

- 5th of June 2023

- There will be no exams during the summer break

- The planned dates for fall 2023 are October 2nd, and November 11th

General Things

- The exam covers all the content of the lecture including **SystemVerilog** and **RISC-V assembly**

- You are expected to be able to **understand and write** short SystemVerilog and RISC-V assembly statements

- In case of longer assembly examples, we will provide a list of RISC-V instructions with the exam. Example:
  - LW   rd,rs1,imm
  - ADDI rd,rs1,imm
  - …
  (no functional description will be provided)

Format of the Exam

- There will be **5 questions** with **10 points** each
  - There will typically be subquestions (a), (b), (c), …
  - Typically there will be 1-2 questions on the network part presented by Jakob Heher

- Grading of the exam
  - 45 - 50 pts. → very good (1)
  - 39 - 44 pts. → good (2)
  - 32 - 38 pts. → satisfactory (3)
  - 26 - 31 pts. → sufficient (4)
  - 00 - 25 pts. → insufficient (5)

Example Questions from Previous Years

- The exams of the courses of the previous years are available online:

- https://www.iaik.tugraz.at/CON/

Evaluation

- Please evaluate this lecture and the practicals

- Your feedback is highly appreciated

Recommended Courses

- Digital System Design

- Digital System Integration and Programming

For Bachelor and Master theses in the field, simply send an email to
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