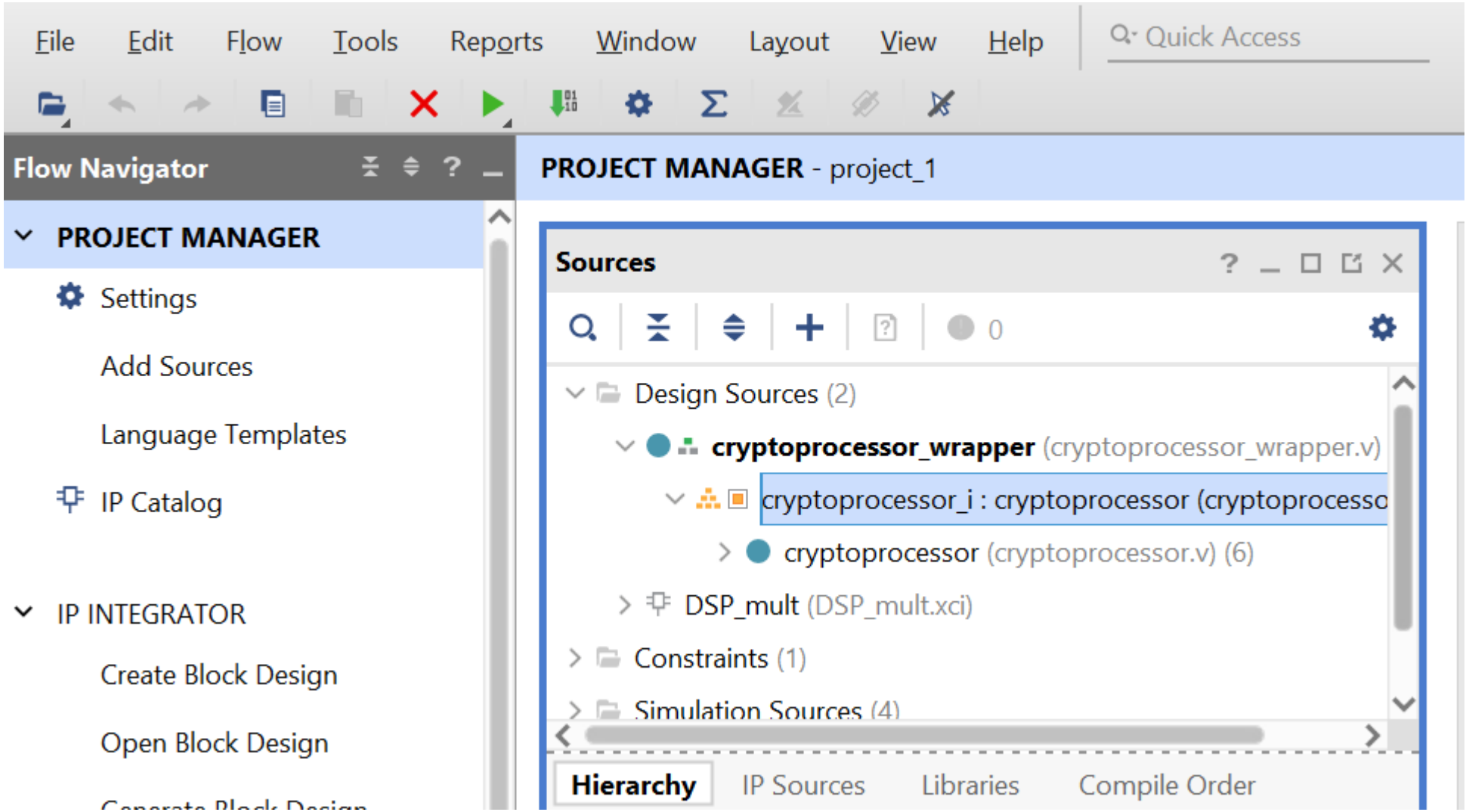
# Changing clock frequency in Vivado

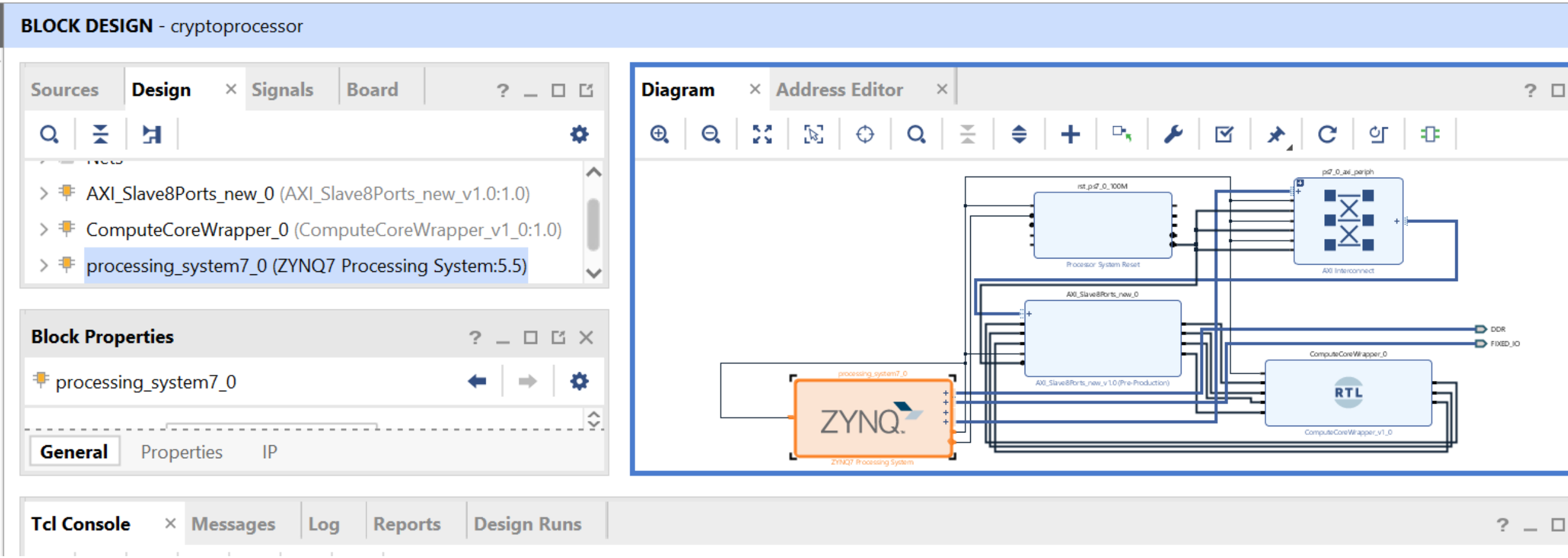
**Warning: Changing the clock frequency can lead to various problems.**

**Therefore, this modification is not recommended.**

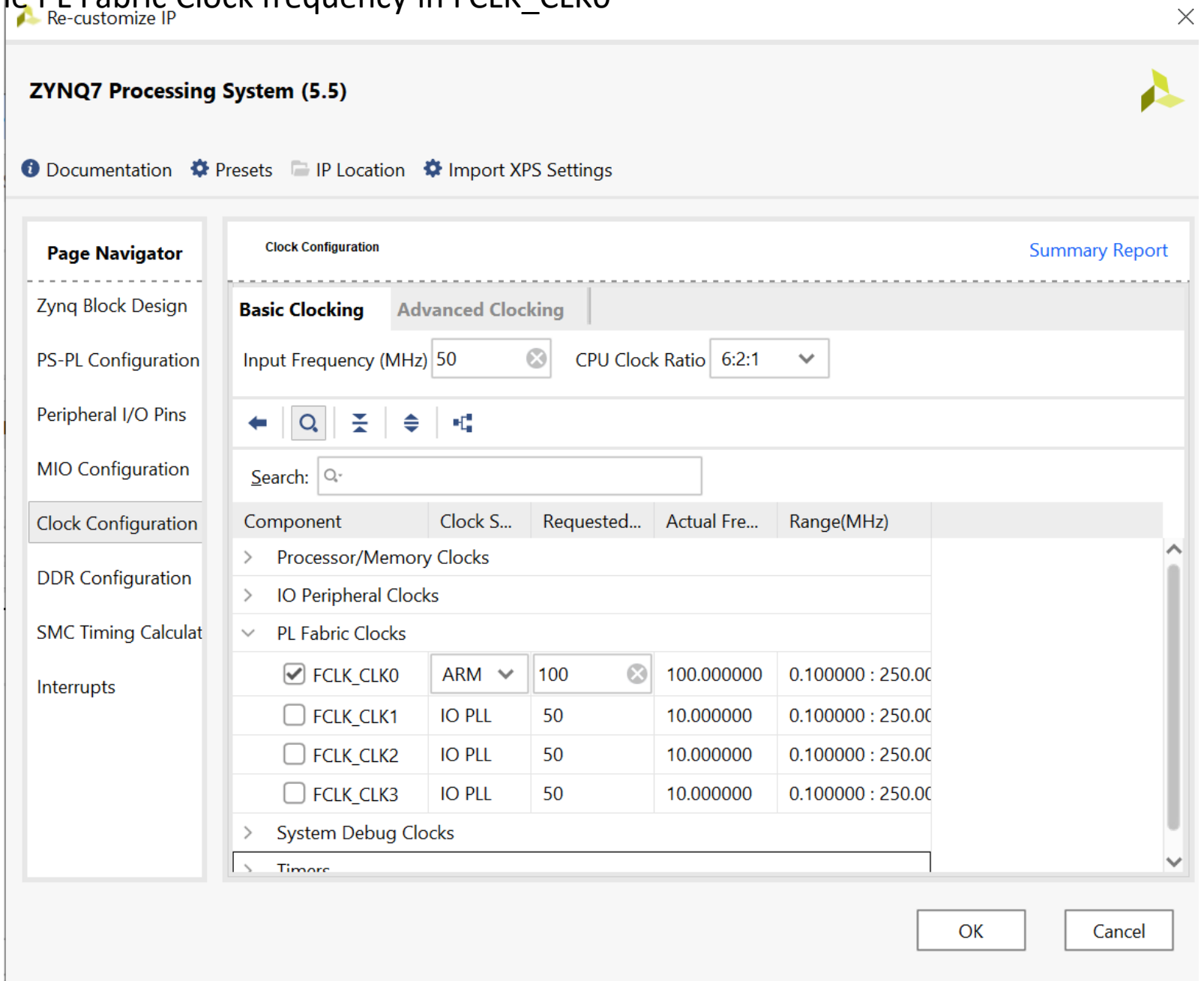
Step1: Open the block diagram of the cryptoprocessor



Step 2: Open the Zynq IP



Step3: Change the PL Fabric Clock frequency in FCLK_CLK0



Save the change in block design and then compile the project for the new clock frequency.