

Computer Organization and Networks

(INB.06000UF, INB.07001UF)

Chapter 13: Information on the Exam

Winter 2021/2022



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The Written Exams

- All written exams take 90 minutes
- Questions are in English
- You can write your answers in English or German
- Paper and pen only (no calculator, mobile, ...) – no red ink, no pencil

Important!

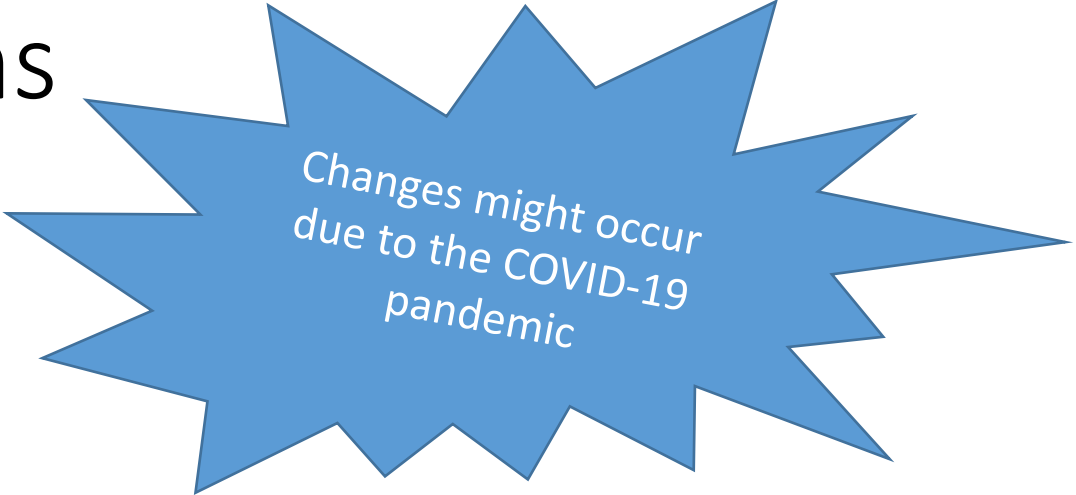
- **Please deregister from an exam immediately, if you decide to not take part in an exam!!!**
- Doing exams during the COVID-19 pandemic is a significant effort for all students and teachers.
- There is no need to add extra effort because of “no-shows”

COVID-19 Regulations

- The exams will take place **on campus**
- Read and follow all **COVID-19 regulations** of TU Graz: <https://www.tugraz.at/en/studying-and-teaching/international-studying-and-teaching/internationals-at-tu-graz-covid-19-information/#c315783>
- Do a **Checkin** to the exam
 - Open the app via [checkin.tugraz.at](https://www.tugraz.at/checkin) with your smartphone or computer.
 - Use this app to scan the QR code from your assigned seat or at the lecture podium to complete the registration, or you can enter your data manually into the system.
 - If a positive coronavirus case is registered in the direct vicinity of your seat, you will be contacted by TU Graz by e-mail.
 - More information: <https://www.tugraz.at/icoe/coronavirus/faq-studierende-students/faq-studienjahr-202021-academic-year-202021/#c350877>
- **Do not take part in an exam, if you feel sick (you do not need to bring a confirmation of a doctor – if you can't deregister yourself, send an email and we will deregister you)**

Currently Planned Exams

- 9th of February 2022
- 23rd of February 2022
- 28th of April 2022
- 23rd of June 2022
- There will be no exams during the summer break
- We plan two exams in fall 2022 (dates will be announced via TUGonline)



Changes might occur
due to the COVID-19
pandemic

General Things

- The exam covers all the content of the lecture including **SystemVerilog** and **RISC-V assembly**
- You are expected to be able to **understand and write** short SystemVerilog and RISC-V assembly statements
- In case of longer assembly examples, we will provide a list of RISC-V instructions with the exam. Example:
 - LW rd,rs1,imm
 - ADDI rd,rs1,imm
 - ...(no functional description will be provided)

Format of the Exam

- There will be **5 questions** with **10 points** each
 - There will typically be subquestions (a), (b), (c), ...
 - Typically there will be 1-2 questions on the network part presented by Johannes Feichtner
- Grading of the exam
 - 45 - 50 pts. → very good (1)
 - 39 - 44 pts. → good (2)
 - 32 - 38 pts. → satisfactory (3)
 - 26 - 31 pts. → sufficient (4)
 - 00 - 25 pts. → insufficient (5)

Example Questions from Previous Years

- The exams of the courses of the previous years are available online:
- <https://www.iaik.tugraz.at/course/computer-organization-and-networks-inb06000uf-wintersemester-2020-21/>
- <https://www.iaik.tugraz.at/course/computer-organization-and-networks-inb06000uf-wintersemester-2021-22/>

Evaluation

- Please evaluate this lecture and the practicals
- Your feedback is highly appreciated

Recommended Courses

- Digital System Design
- Digital System Integration and Programming

For Bachelor and Master theses in the field, simply send an email to stefan.mangard@iaik.tugraz.at