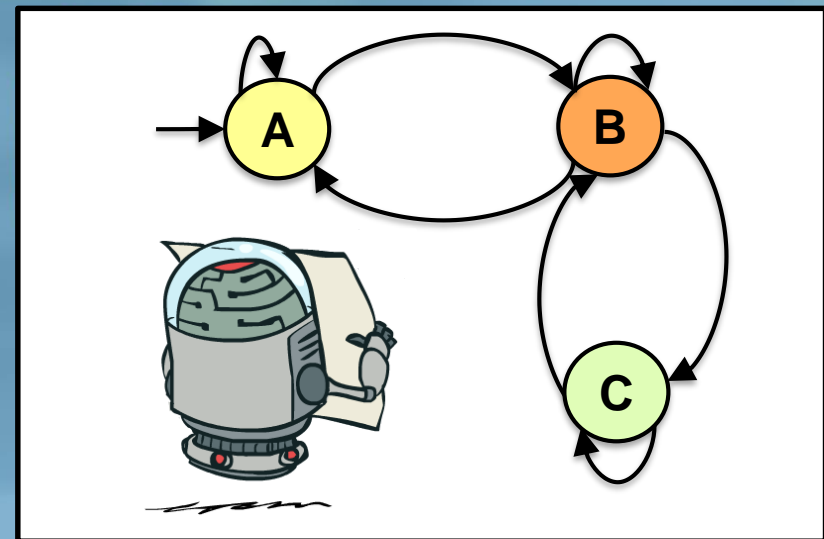
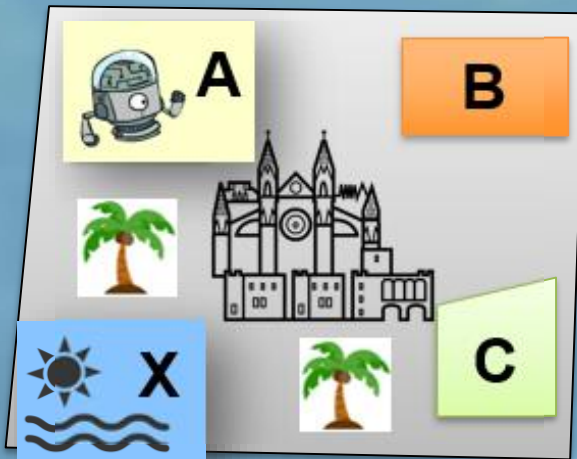


Temporal Logic – Part 2

Bettina Könighofer

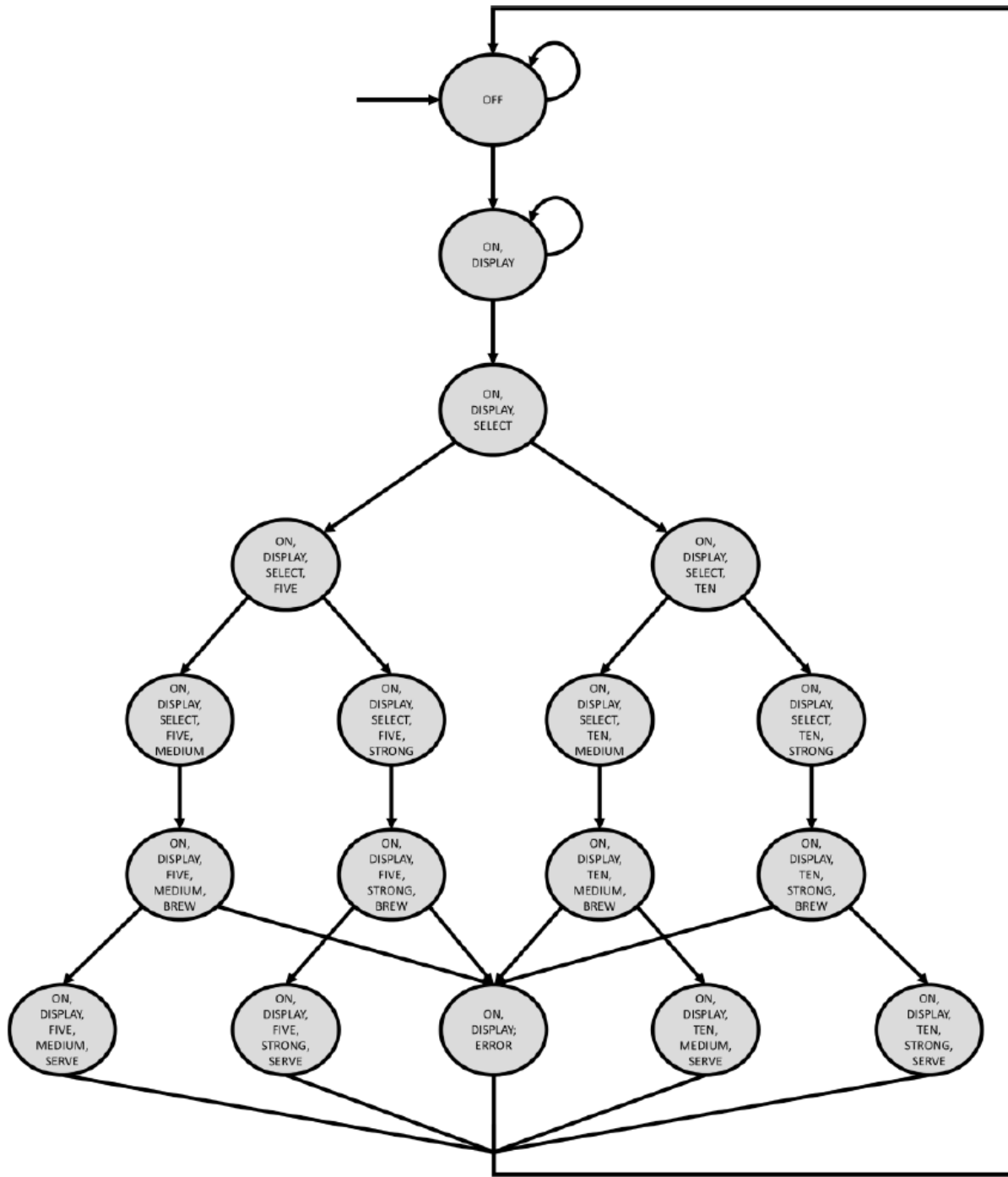


Solutions Homework

The Coffee-Machine-Verification-Problem

Given the following description of a coffee machine.

- The brewer serves **either five or ten cups** of coffee in **either medium or strong** flavour.
- Details:
 - The brewer is normally in the **off state** until it is **switched on**.
 - As long as the coffee machine is switched on, **the display is turned on**.
 - Once the brewer is switched on, the user can **select** the number of cups of coffee and the strength of coffee. The user can either select five or ten cups in either medium or strong flavour.
 - Once the selections have been made, the coffee machine starts the **brewing**.
 - During brewing, if any **error** is detected (say not enough coffee or no milk power), the brewer enters an error state.
 - Alternatively, the brewer is able to finish brewing and can **serve** the coffee
 - Finally, after serving or reaching an error, the coffee machine can be turned off to be eventually turned on again.



LTL

State formulas:

- **A**f where f is a **path** formula

Path formulas:

- $p \in AP$
- $\neg f_1, f_1 \vee f_2, f_1 \wedge f_2, \mathbf{X}f_1, \mathbf{G}f_1, \mathbf{F}f_1, f_1 \mathbf{U}f_2, f_1 \mathbf{R}f_2$

where f_1 and f_2 are path formulas

LTL is the set of all **state** formulas

CTL

CTL is the set of all **state** formulas, defined below (by means of state formulas only):

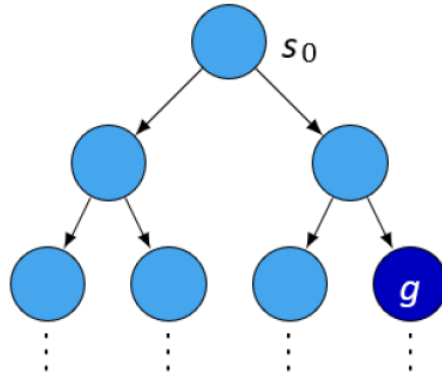
- $p \in AP$
- $\neg g_1, g_1 \vee g_2, g_1 \wedge g_2$
- **AX** $g_1, \mathbf{AG} g_1, \mathbf{AF} g_1, \mathbf{A} (g_1 \mathbf{U} g_2), \mathbf{A} (g_1 \mathbf{R} g_2)$
- **EX** $g_1, \mathbf{EG} g_1, \mathbf{EF} g_1, \mathbf{E} (g_1 \mathbf{U} g_2), \mathbf{E} (g_1 \mathbf{R} g_2)$

where g_1 and g_2 are state formulas

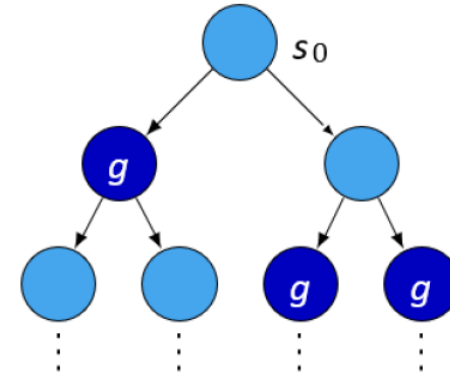
Illustration of CTL Semantics

EFg

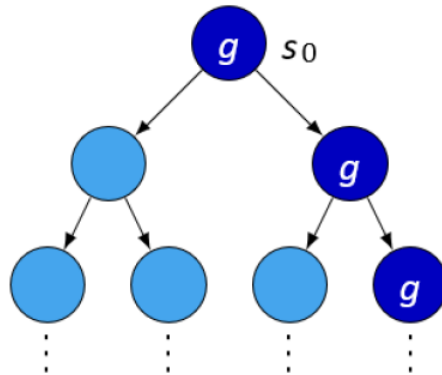
“exists
reachable
state such
that...”



AFg

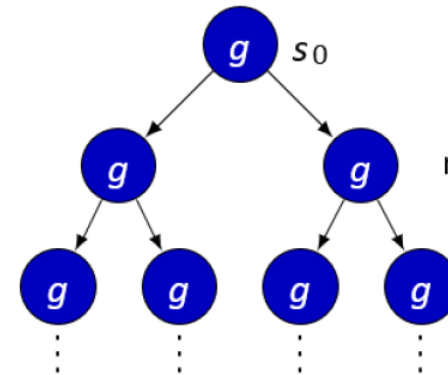


EGg



AGg

“all
reachable
states...”

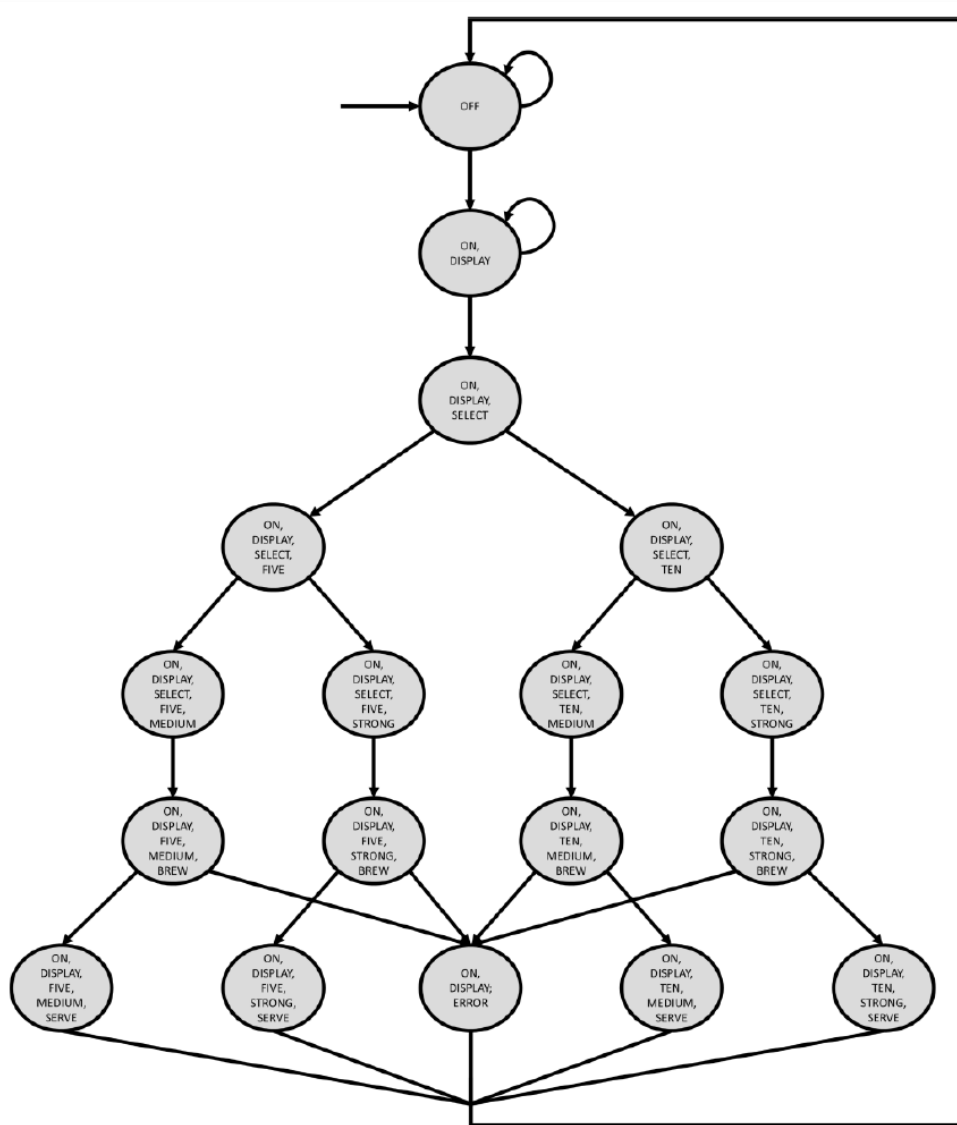


Properties in LTL/CTL/CTL*



- 1 From any state one can possibly eventually select ten cups of coffee and once selected, ten cups will always be served (or an error encountered) in the future.

$$f_{1,CTL} = (AG EF coffee_10) \wedge (AG(coffee_10 \rightarrow AF(serve_10_medium \vee serve_10_strong \vee error)))$$



Yes

- 1 From any state one can possibly eventually select ten cups of coffee and once selected, ten cups will always be served (or an error encountered) in the future.

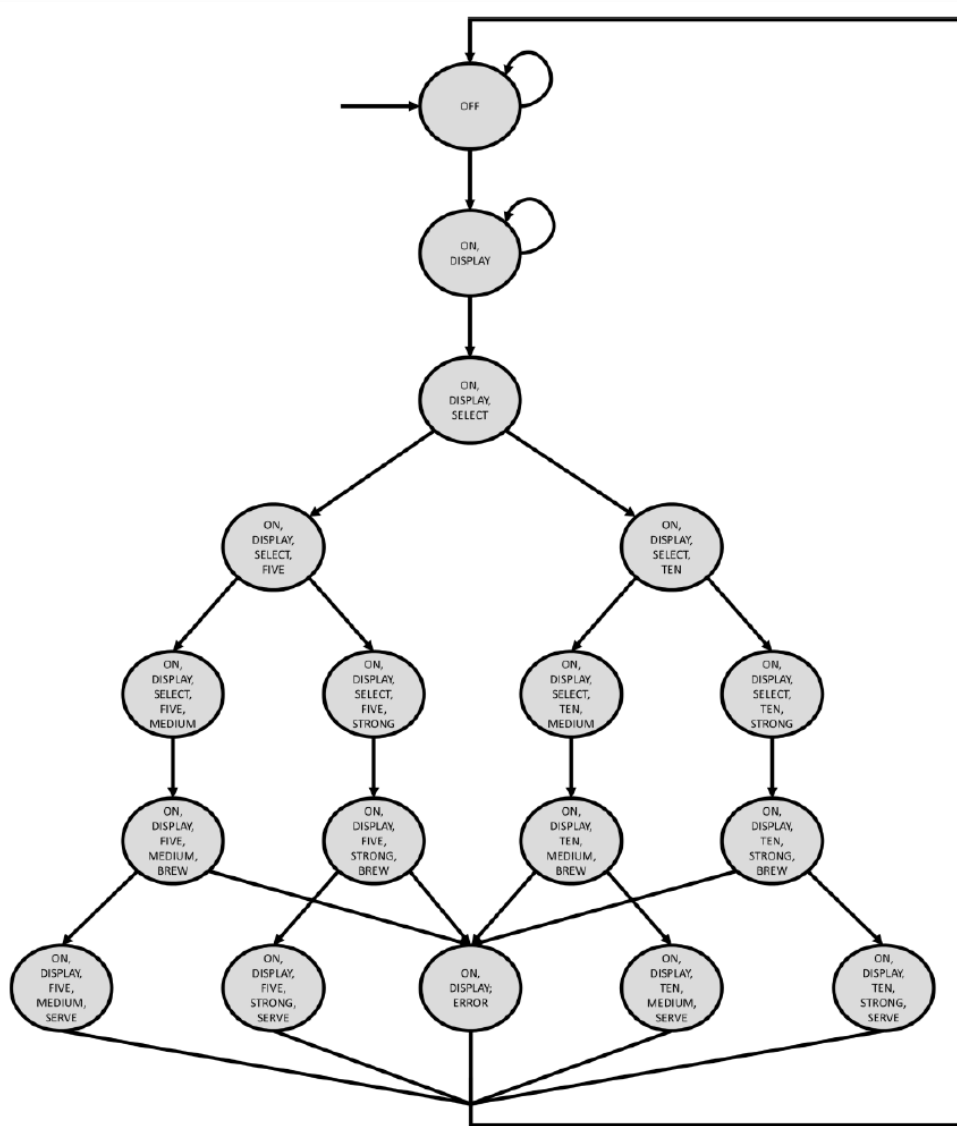
$$f_{1,CTL} = (AG EF coffee_{10}) \wedge (AG(coffee_{10} \rightarrow AF(serve_{10_medium} \vee serve_{10_strong} \vee error)))$$

Properties in LTL/CTL/CTL*



- 2 It is possible to eventually reach an error for any selection of coffee strength or number of cups made.

$$f_{2,CTL} = AG((coffee_10 \vee coffee_5 \vee str_medium \vee str_strong) \rightarrow EF\ error)$$



Yes

- 2 It is possible to eventually reach an error for any selection of coffee strength or number of cups made.

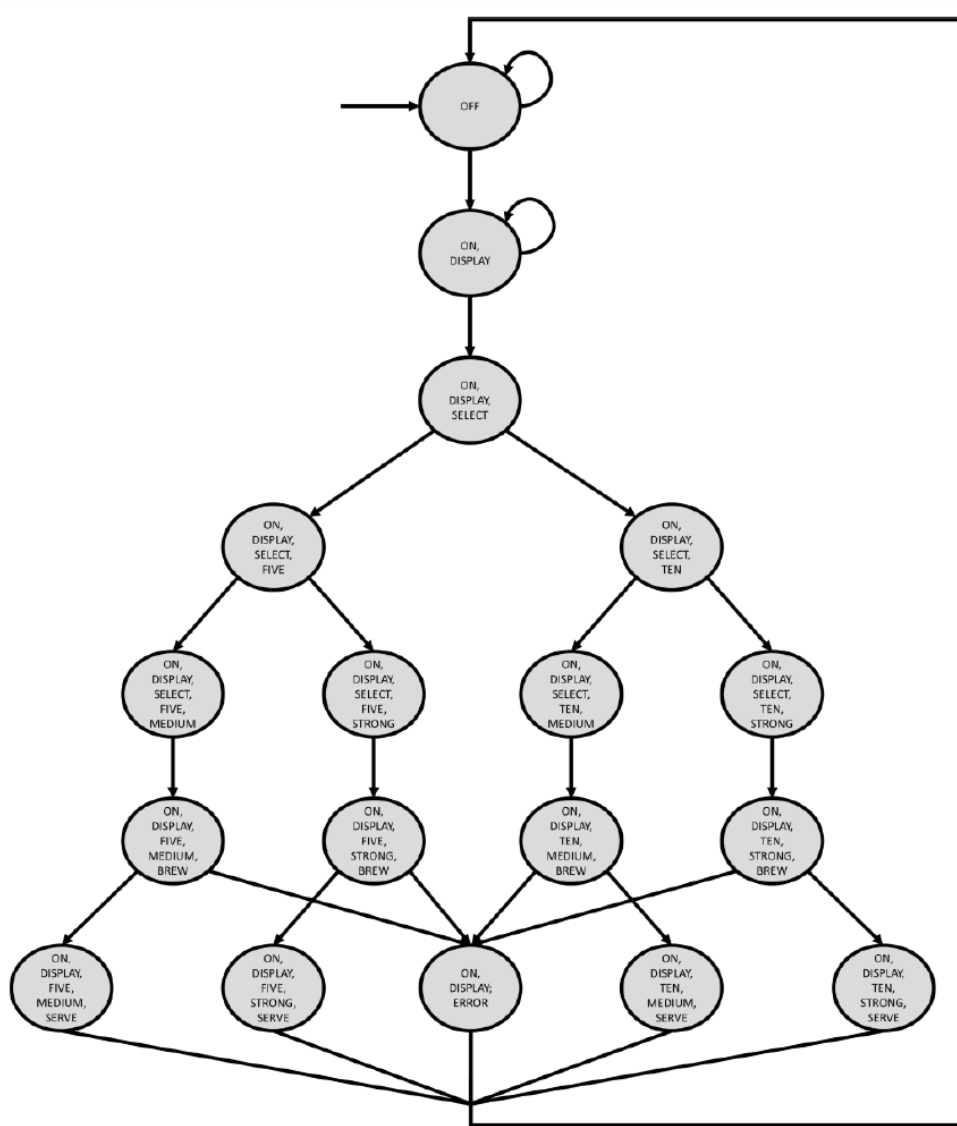
$$f_{2,CTL} = AG((coffee_10 \vee coffee_5 \vee str_medium \vee str_strong) \rightarrow EF\ error)$$

Properties in LTL/CTL/CTL*



- 3 For any execution, the coffee machine will forever be turned off from some point on.

$$f_{3,CTL} = AG EF (AG \neg on)$$



No

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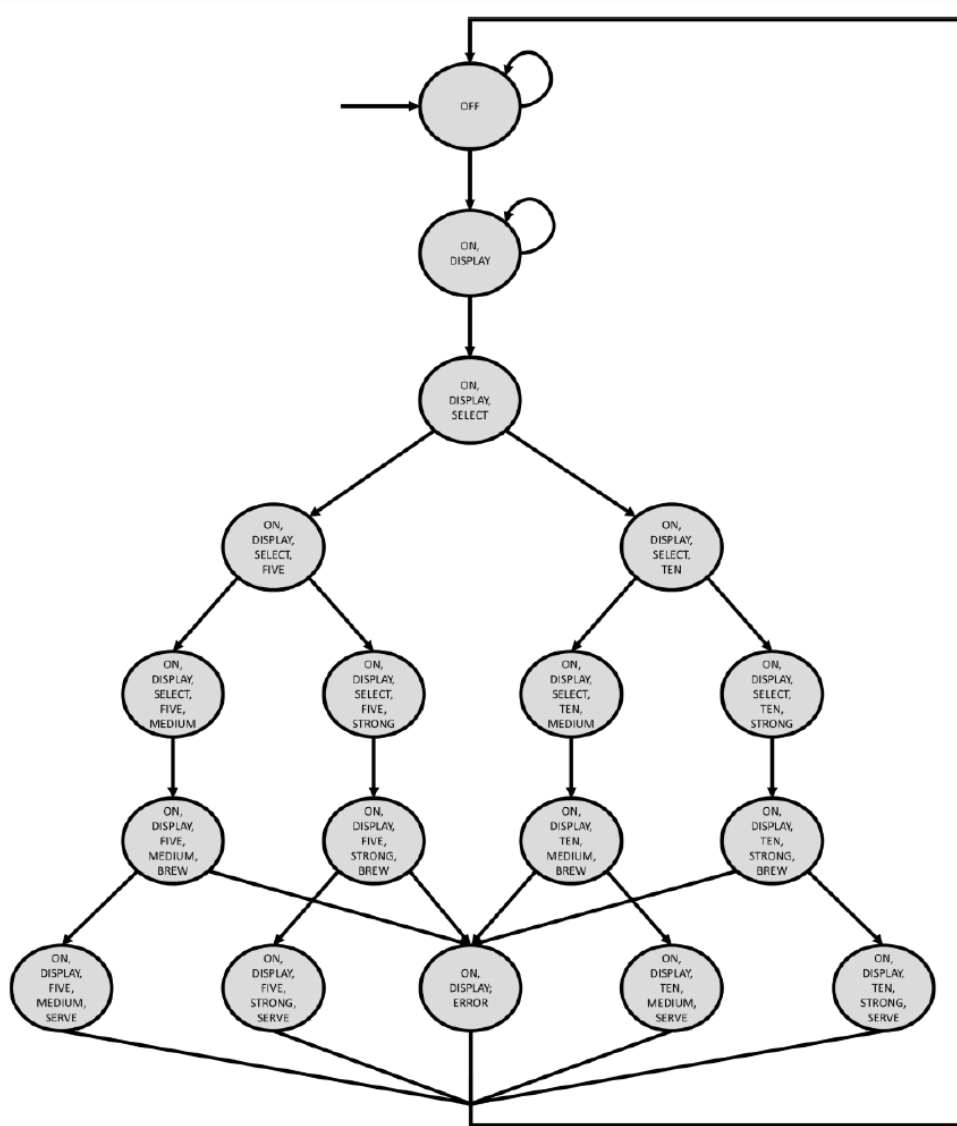
$$f_{3,CTL} = AG EF (AG \neg on)$$

Properties in LTL/CTL/CTL*



- 4 Before the coffee brewer gets turned off forever which will always happen eventually, there was eventually coffee.

$$f_{4,LTL} = A((F(\text{serve_10_medium} \vee \text{serve_10_strong} \vee \text{serve_5_medium} \vee \text{serve_5_strong})U(G \neg \text{on})))$$



No

4 Before the coffee brewer gets turned off forever which will always happen eventually, there was eventually coffee.

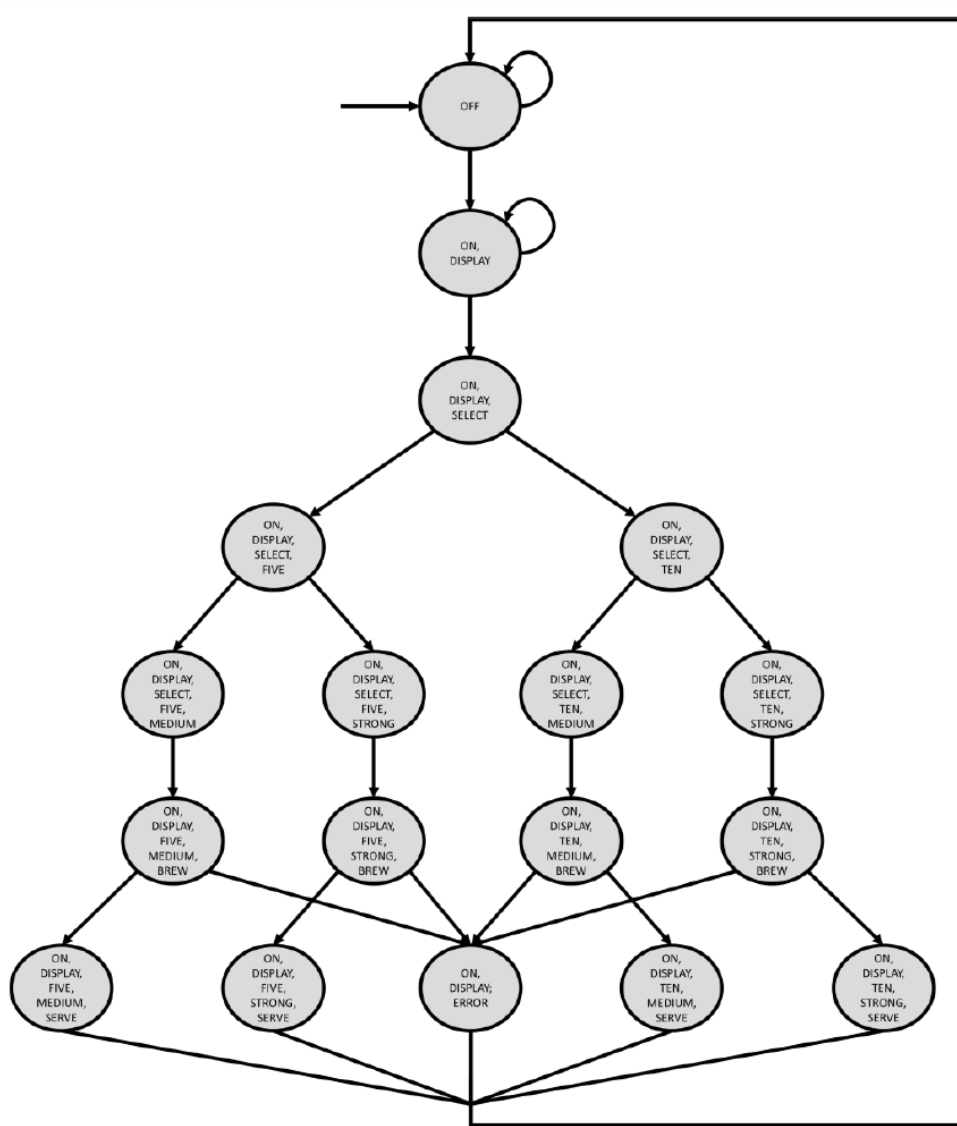
$$f_{4, LTL} = A((F(serve_{10_medium} \vee serve_{10_strong} \vee serve_{5_medium} \vee serve_{5_strong})U(G \neg on)))$$

Properties in LTL/CTL/CTL*



- 5 Always, once the brewing is done, the display lighting is eventually turned off.

$$f_{5,LTL} = A(\text{brew} \rightarrow F\neg\text{display_on})$$



Yes

5 Always, once the brewing is done, the display lighting is eventually turned off.

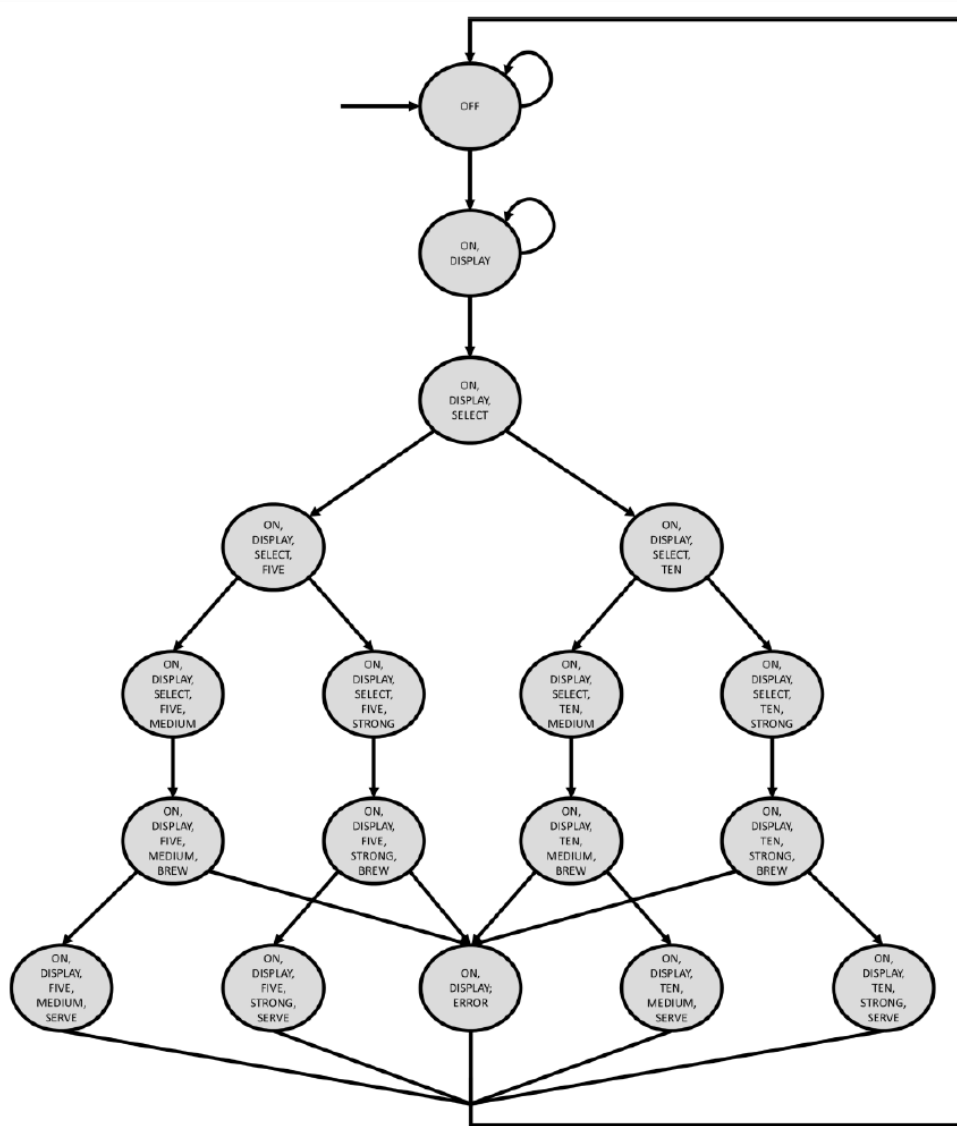
$$f_{5, LTL} = A(\text{brew} \rightarrow F \neg \text{display_on})$$

Properties in LTL/CTL/CTL*



- 6 It can be the case that we reach an error, but get eventually 10 cups of coffee nevertheless.

$$f_{6,CTL^*} = EF (\neg(F \text{ error} \rightarrow AG \neg(\text{serve}_{10_medium} \vee \text{serve}_{10_strong})))$$



Yes

6 It can be the case that we reach an error, but get eventually 10 cups of coffee nevertheless.

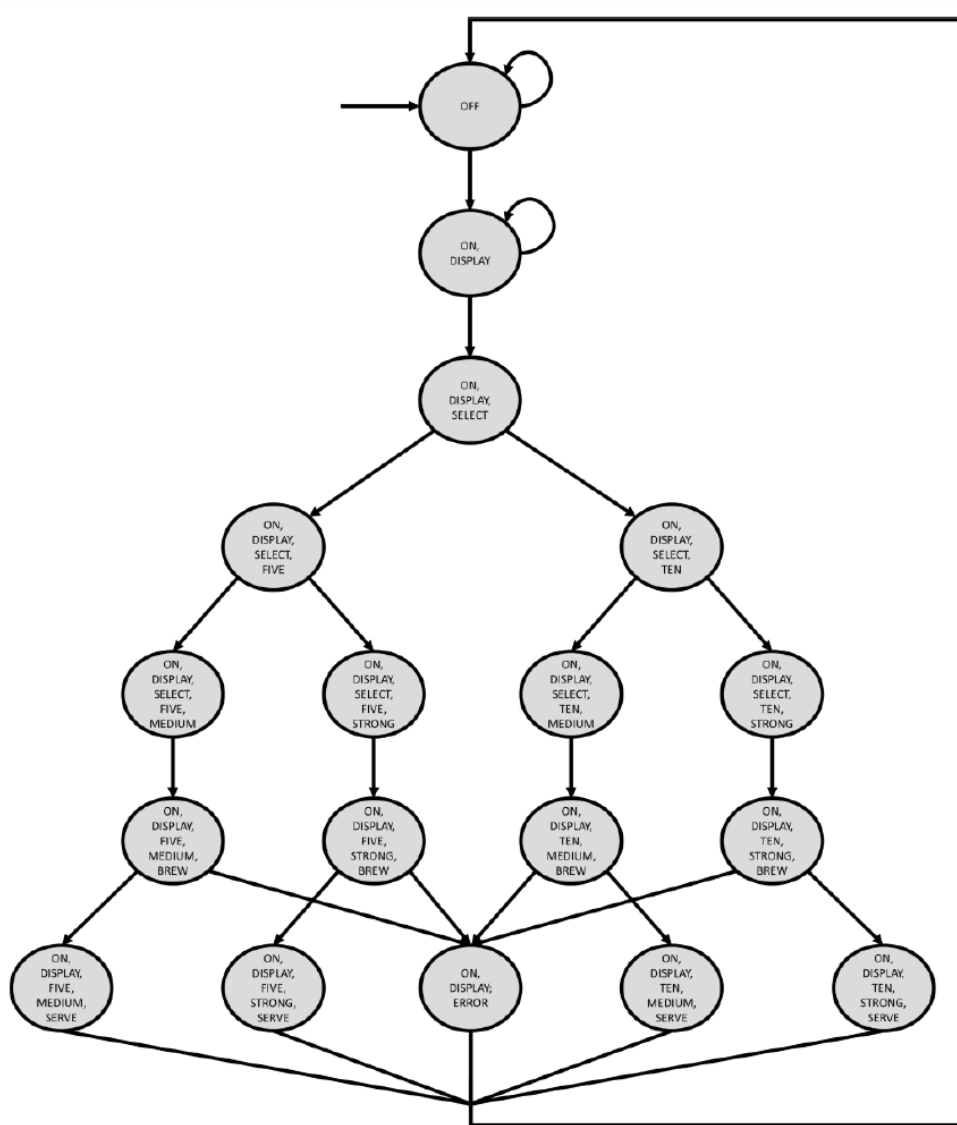
$$f_{6,CTL^*} = EF (\neg(F \text{ error} \rightarrow AG \neg(\text{serve}_{10_medium} \vee \text{serve}_{10_strong})))$$

Properties in LTL/CTL/CTL*



- 7 All reachable states can result in 10 cups of coffee eventually.

$$f_{7,CTL} = EG AF (EF (serve_10_strong \vee serve_10_medium))$$



Yes

7 All reachable states can result in 10 cups of coffee eventually.

$$f_{7,CTL} = EG AF (EF (serve_{10_strong} \vee serve_{10_medium}))$$

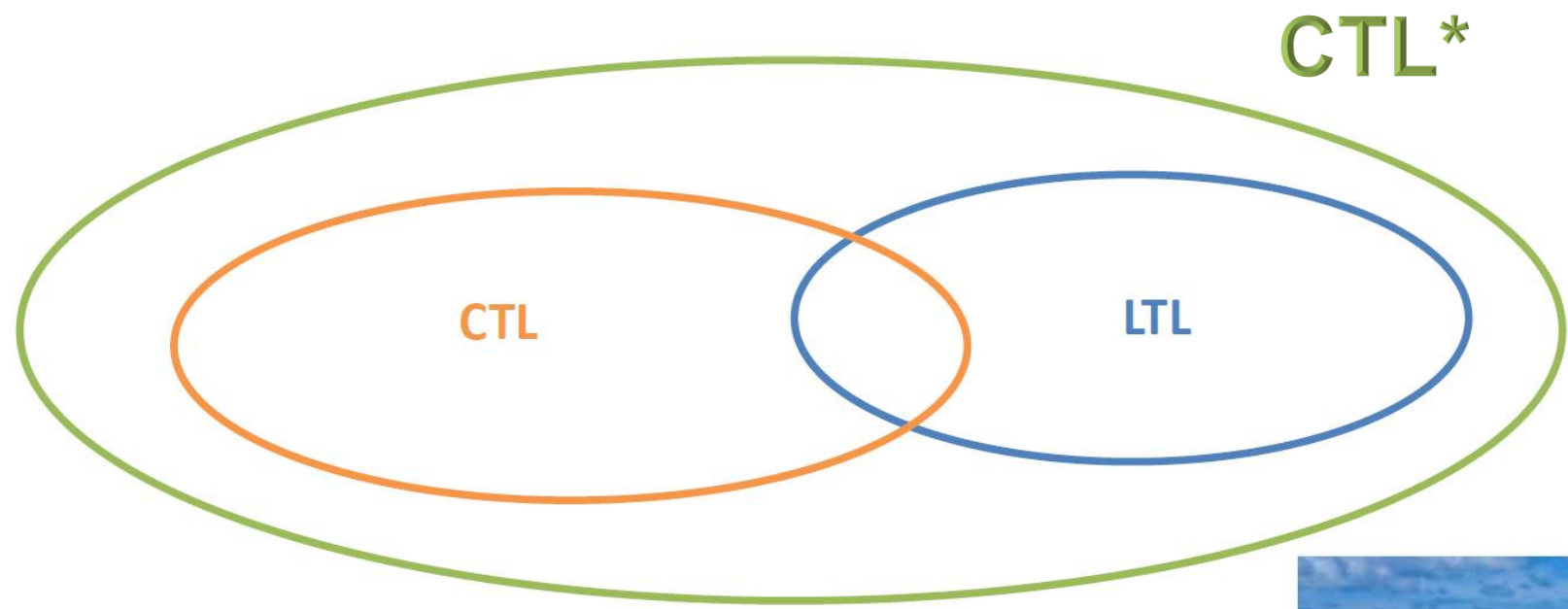


Expressiv Power of LTL and CTL

CTL*



Expressiv Power of LTL and CTL



LTL vs CTL

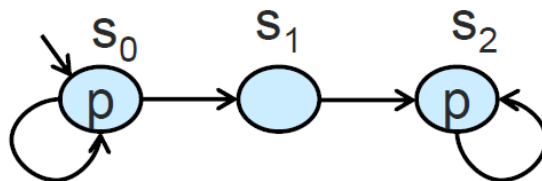


- Exercise:
 - Does the LTL formula $FG p$ have an equivalent in CTL?

LTL vs CTL



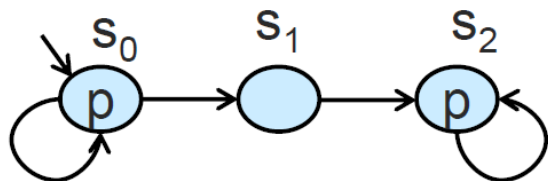
- Exercise:
 - Does the LTL formula $FG p$ have an equivalent in CTL?
- Solution: No
 - Failed attempts: $AFAGp$
 - “in every path there is a point from which all reachable states satisfy p ”



LTL vs CTL



- Exercise:
 - Does the LTL formula ***FG p*** has an equivalent in CTL?
- Solution: No
 - Failed attempts: ***AFAGp***
 - “in every path there is a point from which all reachable states satisfy *p*”



All paths satisfy ***FGp***

- s_0, s_0, s_0, \dots

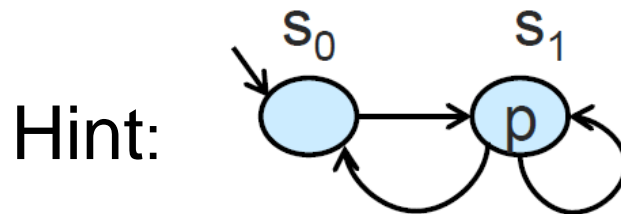
- $s_0, s_0, \dots, s_0, s_1, s_2, s_2, s_2, \dots$

But first one does not sat ***FAGp***

LTL vs CTL

- Exercise:
 - Does the LTL formula $FG p$ have an equivalent in CTL?
- Solution: No

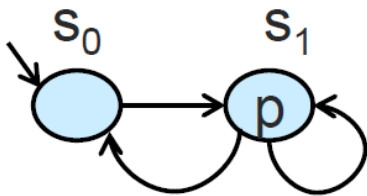
 What about $AFEG p$?



LTL vs CTL



- Exercise:
 - Does the LTL formula ***FG p*** has an equivalent in CTL?
- Solution: No
 - What about ***AFEG p***?
 - “in every path there is a point from which there is a path where p globally holds”



All paths satisfy ***FEGp***

- since s_1 sat ***EGp***

But $s_0, s_1, s_0, s_1, s_0, s_1, \dots$ does not satisfy ***FGp***

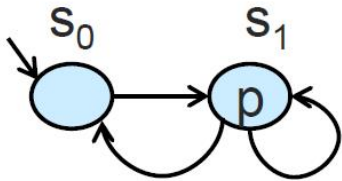
LTL vs CTL



- Exercise:
 - Does $AG(EF p)$ has an LTL equivalent?

LTL vs CTL

- Exercise:
 - Does **AG(EF p)** has an LTL equivalent?
- Solution: No
 - Failed attempt:
 - **GFp** : “in all paths, p holds infinitely many times.”



All reachable states (s_0, s_1) satisfy **EFp**

But s_0, s_0, s_0, \dots does not satisfy **GFp**



LTL vs CTL

- The expressive powers of LTL and CTL are incomparable. That is,
 - There is an LTL formula that has no equivalent CTL formula
 - There is a CTL formula that has no equivalent LTL formula
- CTL* is more expressive than either of them

Counterexamples

- Given M and φ , such that $M \neq \varphi$, a **counterexample** is a behavior of M , demonstrating the **violation of φ in M**
- To be useful for debugging it should
 - **have finite representation**
 - **be easy-to-understand by human**

Examples of Counterexamples

- For AXp :
A transition from an initial state to a state violating p
 - Counterexample for AXp is a witness for $EX\neg p$

Examples of Counterexamples

- For **AXp**:
A transition from an initial state to a **state violating p**
 - Counterexample for **AXp** is a witness for **EX¬p**

- For **AGp**:
A finite path from an initial state to a **state violating p**
 - Counterexample for **AGp** is a witness for **EF¬p**



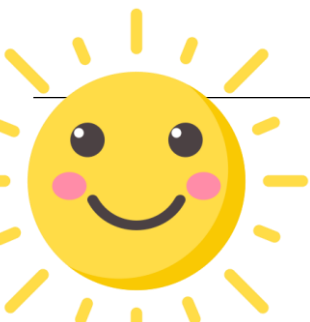
Examples of Counterexamples

- For **AFp**:
An infinite path, all of its states **violating p** (satisfying $\neg p$)
 - **Counterexample** for **AFp** is a witness for **EG $\neg p$**



Exercise:

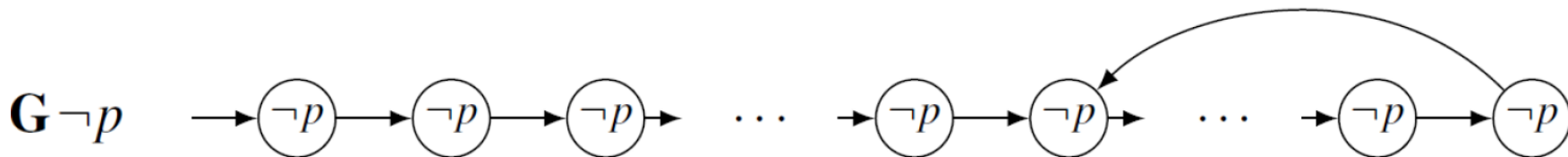
- How do we get a finite representation for the CE?



Examples of Counterexamples

- For **AFp**:
An infinite path, all of its states **violating p** (satisfying $\neg p$)
 - Counterexample for **AFp** is a witness for **EG $\neg p$**

- A finite representation for violation of AFp:
 - A **lasso**, which is a path of the form $\pi = \pi_0 (\pi_1)^\omega$
 - π_0 and π_1 are **finite paths**
 - ω indicates infinitely many repetitions of π_1



Safety and Liveness Properties

Safety and Liveness Properties

Informally,

- **Safety** properties guarantee that “something wrong will never happen”
 - Typical example: **AGp**

Safety and Liveness Properties

Informally,

- **Safety** properties guarantee that “something wrong will never happen”
 - Typical example: **AGp**
- **Liveness** properties guarantee that “something good will eventually happen”
 - Typical examples: **AFp**, **A(pUq)**

Safety Properties

- Nothing 'bad' will happen.
 - Example: $G(p \rightarrow X q)$



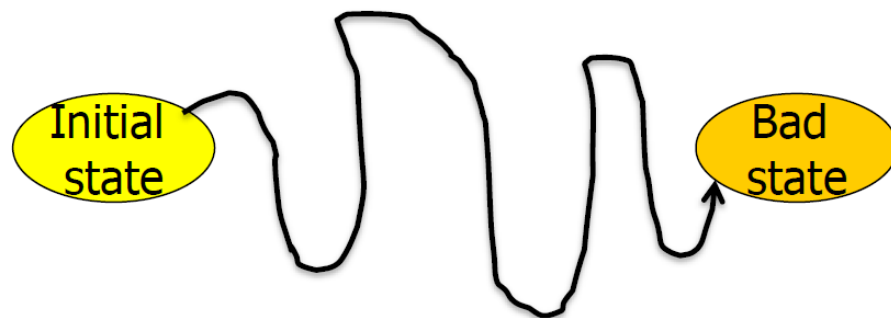
Exercise:

- How does a CE for a **safety** property look like?

Safety Properties

- Nothing 'bad' will happen.
 - Example: $G(p \rightarrow X q)$

- Counterexamples for a **safety** property is a **finite (loop-free) path**



Liveness Properties

- Something 'good' will happen.
 - Example: $F p$

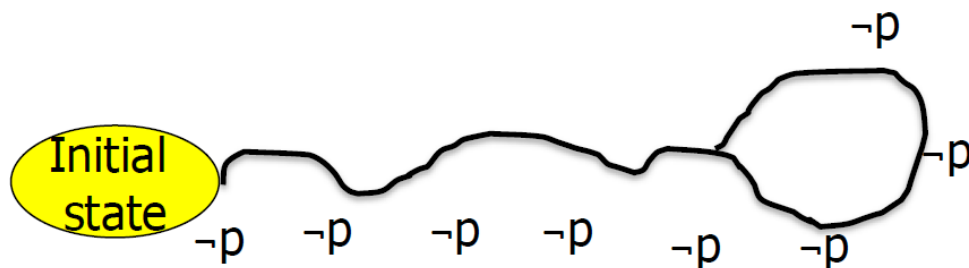


Exercise:

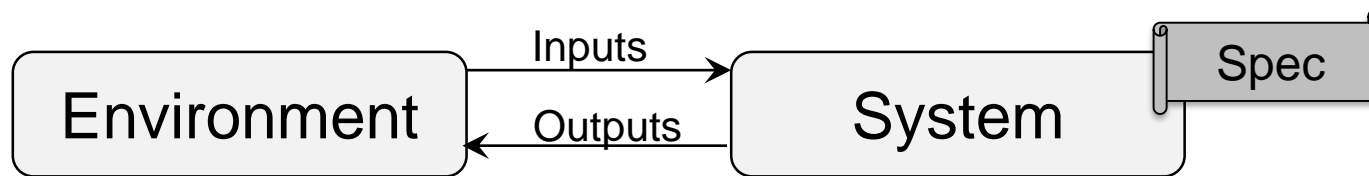
- How does a CE for a **Liveness** property look like?

Liveness Properties

- Something 'good' will happen.
 - Example: $F p$
- A counterexample is an infinite trace with **lasso shape**, showing that this good thing **NEVER** happens.



Reactive Systems

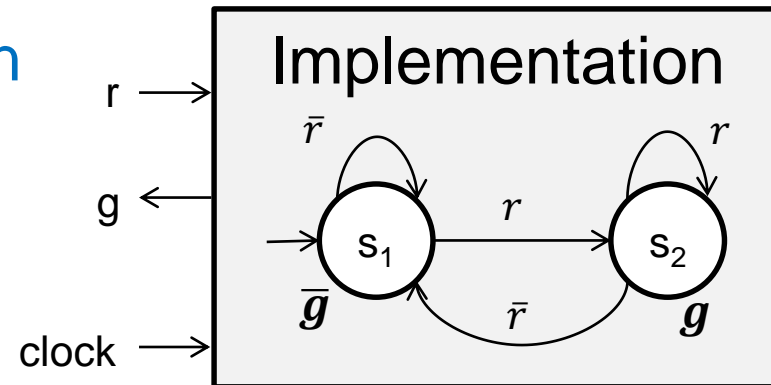


- $I = \{i_1, i_2, \dots, i_m\}$ is a set of Boolean inputs
- $O = \{o_1, o_2, \dots, o_n\}$ is a set of Boolean outputs

Implementation: What is Inside?

Moore Machine:

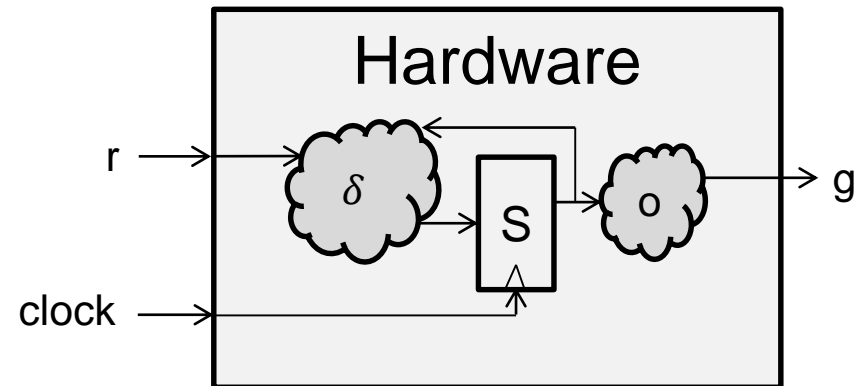
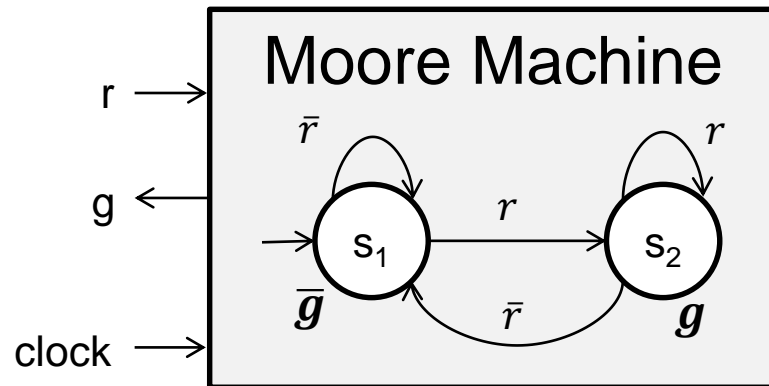
- Input alphabet: $\Sigma_I = 2^I$
- Output alphabet: $\Sigma_O = 2^O$
- A Moore Machine is a tuple $(S, s_0, \Sigma_I, \Sigma_O, \delta, o)$
 - S is a finite set of states
 - s_0 is an initial state
 - $\delta: S \times \Sigma_I \rightarrow S$ is the transition function
 - $o: S \rightarrow \Sigma_O$ is the output function



From a Moore Machine to Hardware

Simple Transformation:

- The current state $s \in S$ is stored in flip-flops
- $\delta: S \times \Sigma_I \rightarrow S$ and $o: S \rightarrow \Sigma_O$ are combinatorial circuits



Example: Arbiter



Input: r0, r1

Output: g0, g1



Draw Moore Machine

Guarantee G1: $G(r0 \rightarrow Fg0)$

Guarantee G2: $G(r1 \rightarrow Fg1)$

Guarantee G3: $G(\neg g0 \vee \neg g1)$

$$\varphi := G_1 \wedge G_2 \wedge G_3$$

Example: Arbiter



Input: r_0, r_1

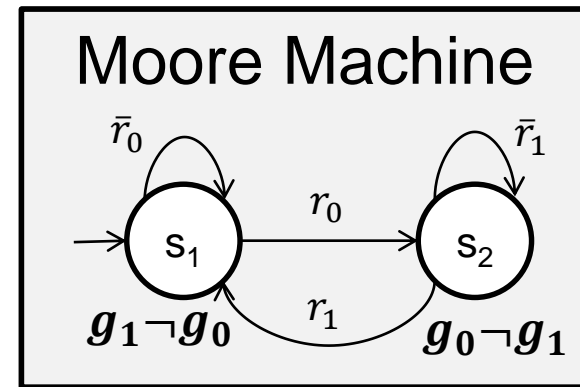
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Example: Arbiter



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Example: Arbiter



Input: r0, r1

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Unrealizable!

Guarantee G1: $G(r0 \rightarrow \mathbf{X}g0)$

Guarantee G2: $G(r1 \rightarrow \mathbf{X}g1)$

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$$\varphi := G_1 \wedge G_2 \wedge G_3$$

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Guarantee G1: $G(r0 \rightarrow Xg0)$

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Assumption A1: $G(\neg g0 \vee \neg g1)$

$$\varphi := A_1 \rightarrow G_1 \wedge G_2 \wedge G_3$$

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Guarantee G1: $G(r0 \rightarrow Xg0)$

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